

FIG. 1

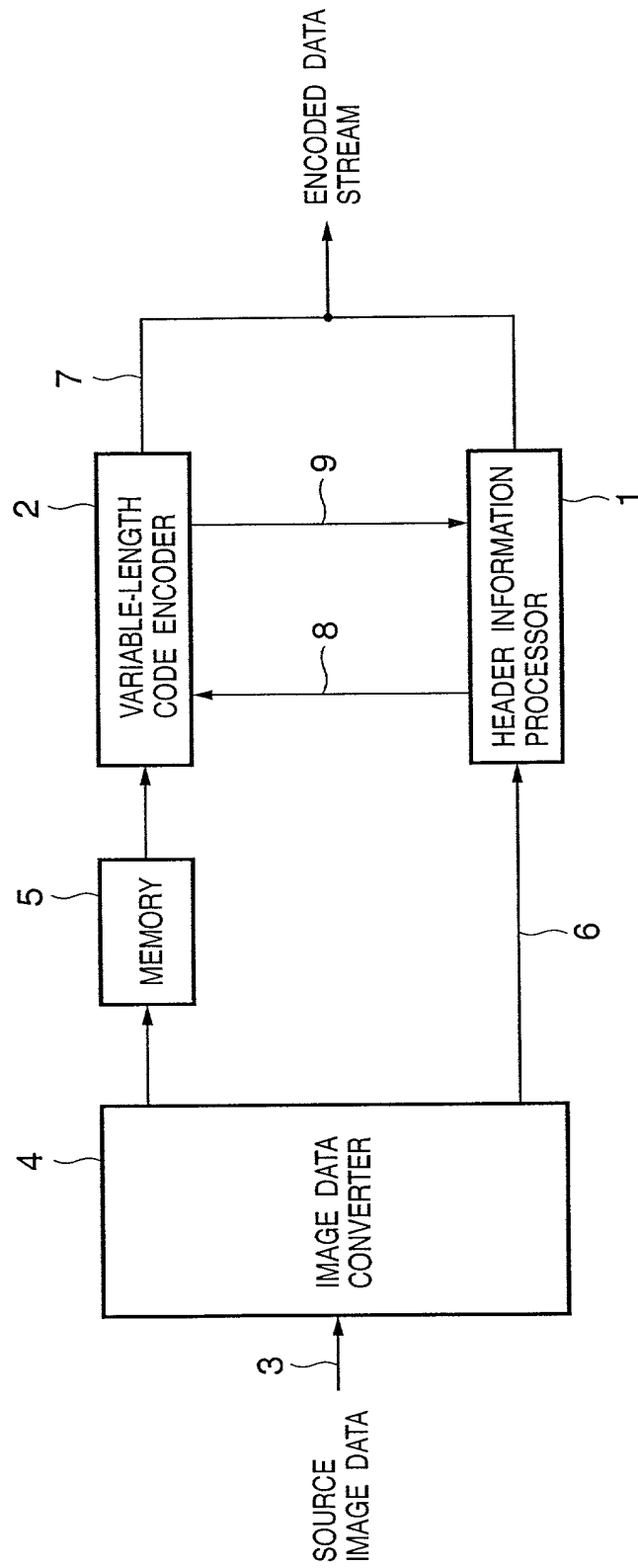
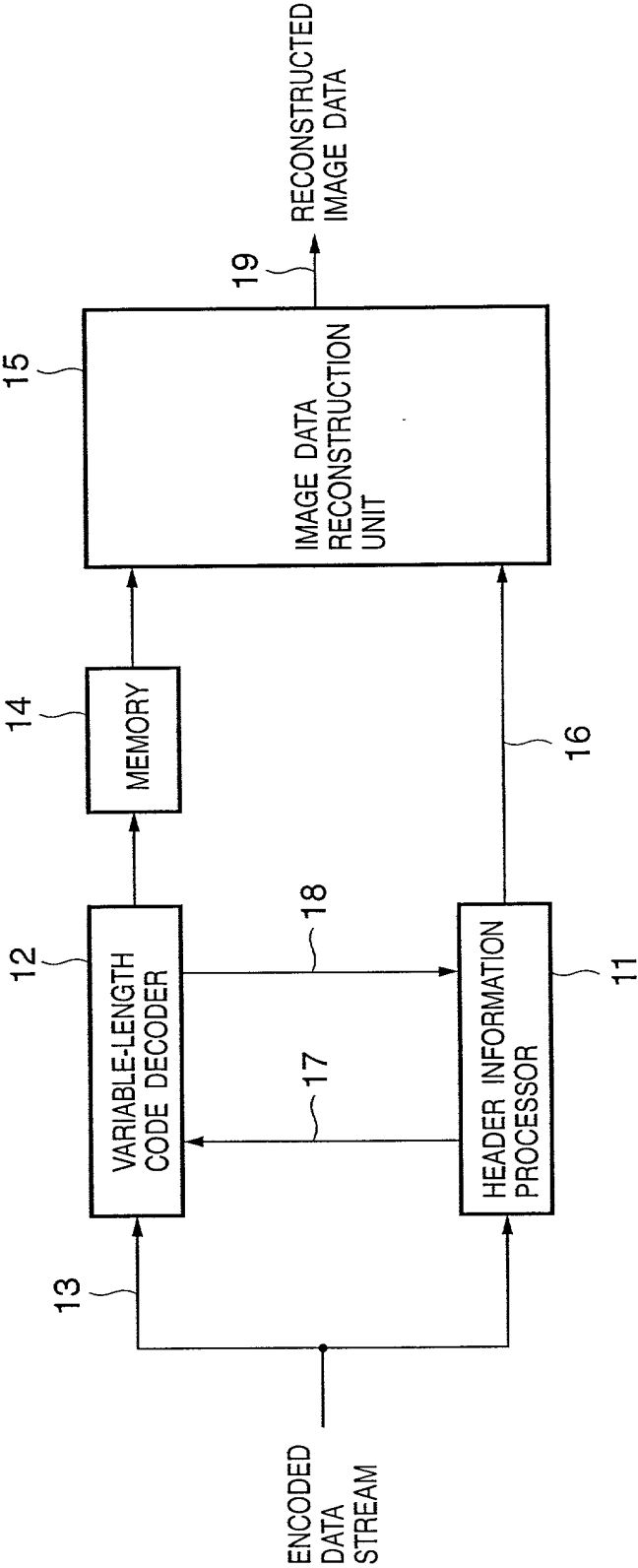
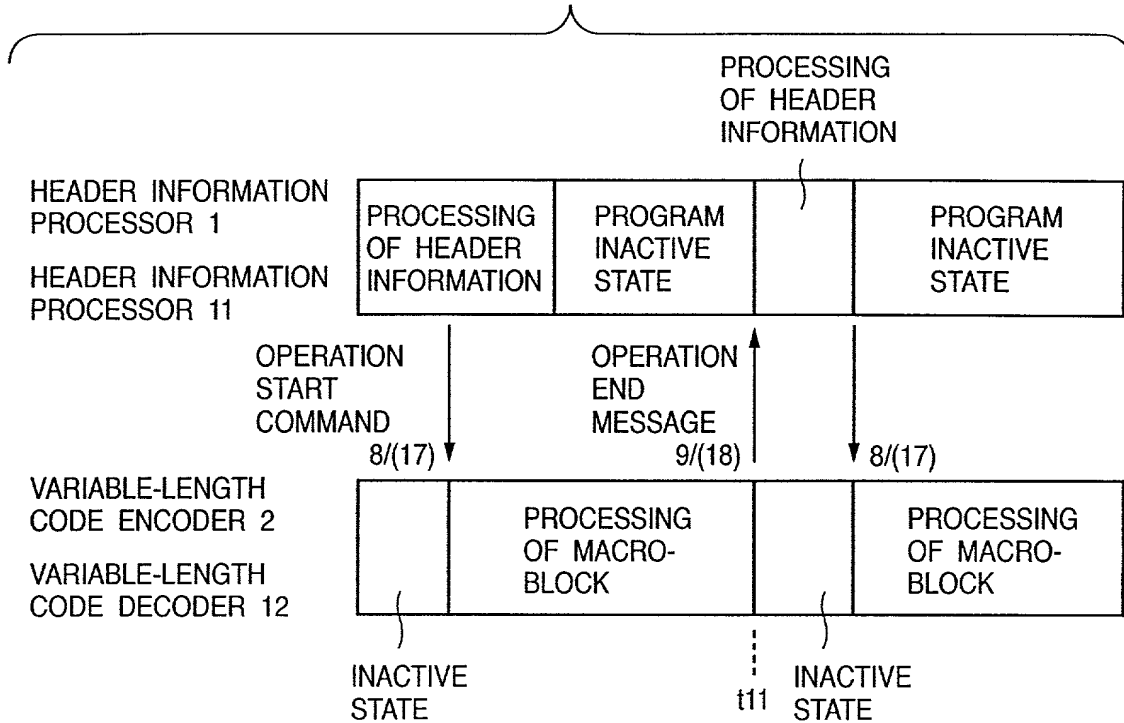
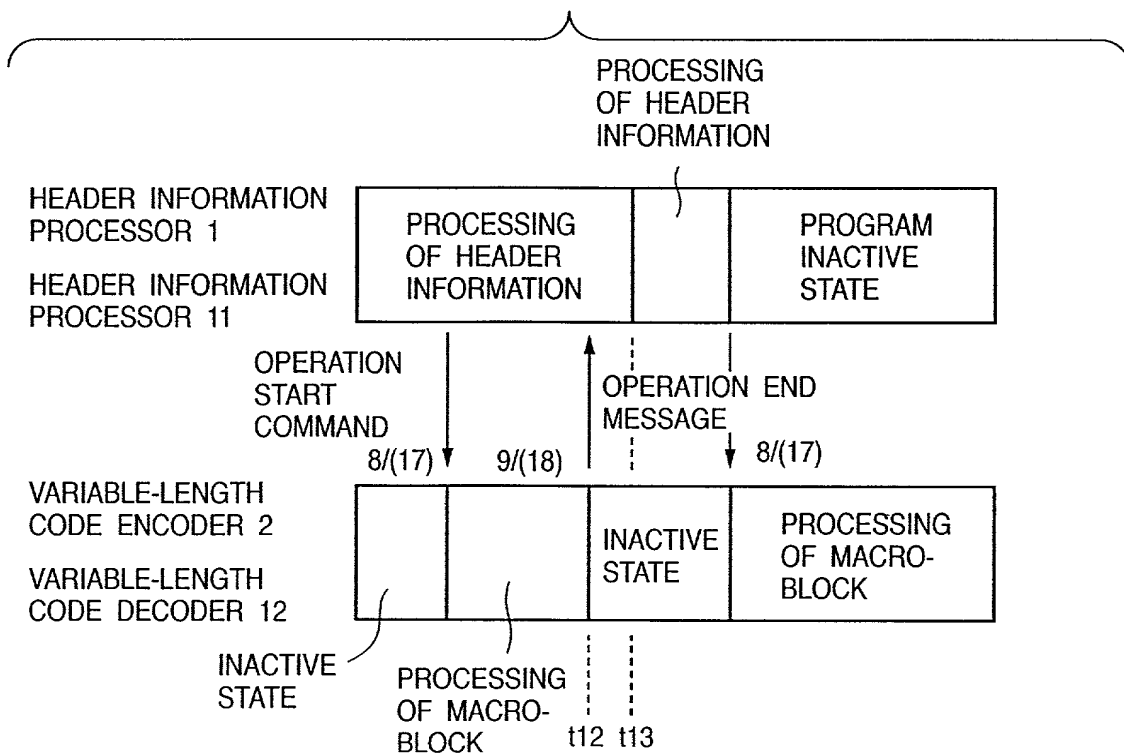


FIG. 2





**FIG. 4A****FIG. 4B**

## FIG. 5

HEADER INFORMATION PROCESSOR 1/11

ENCODER 2/DECODER 12

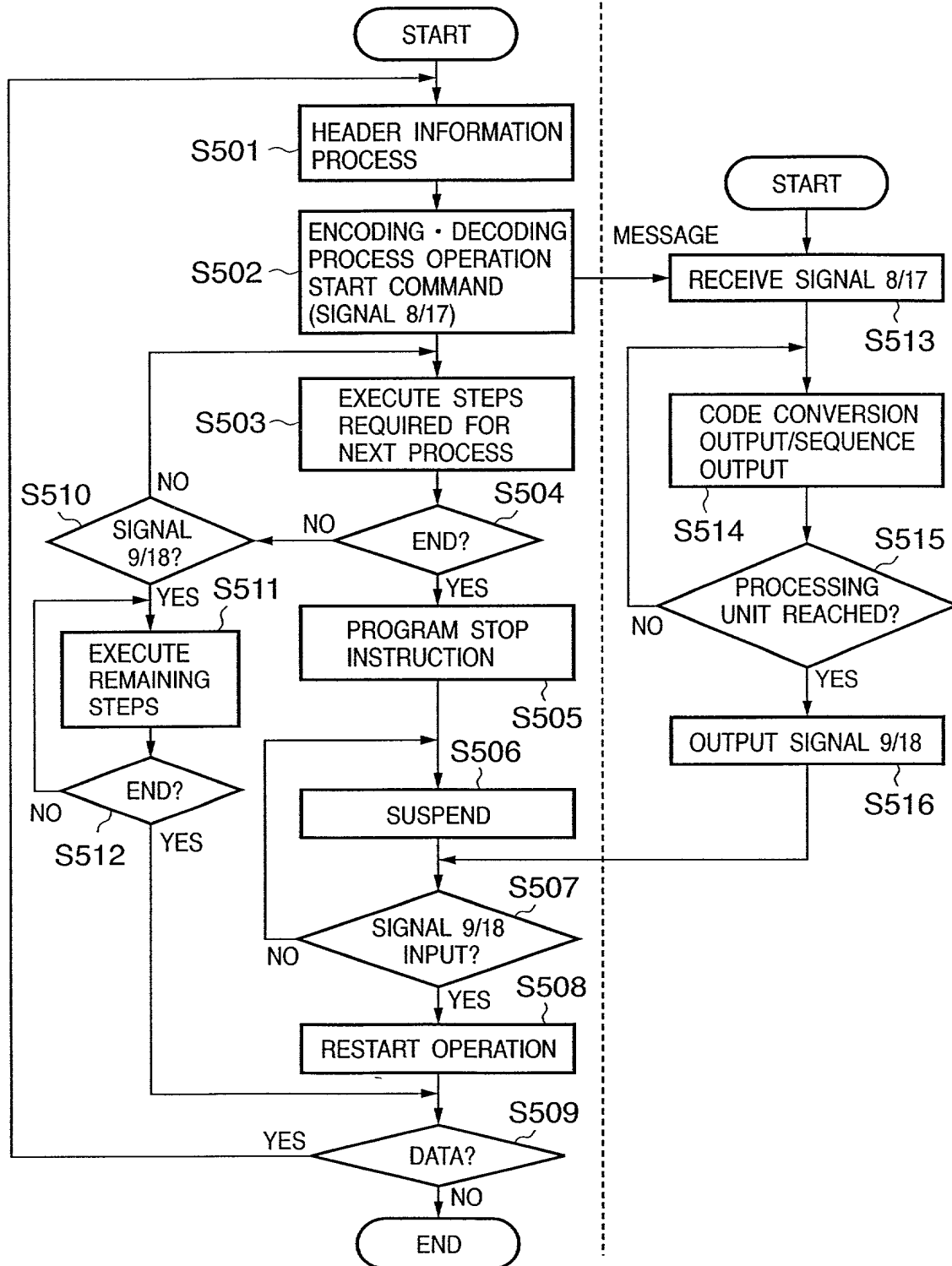


FIG. 6

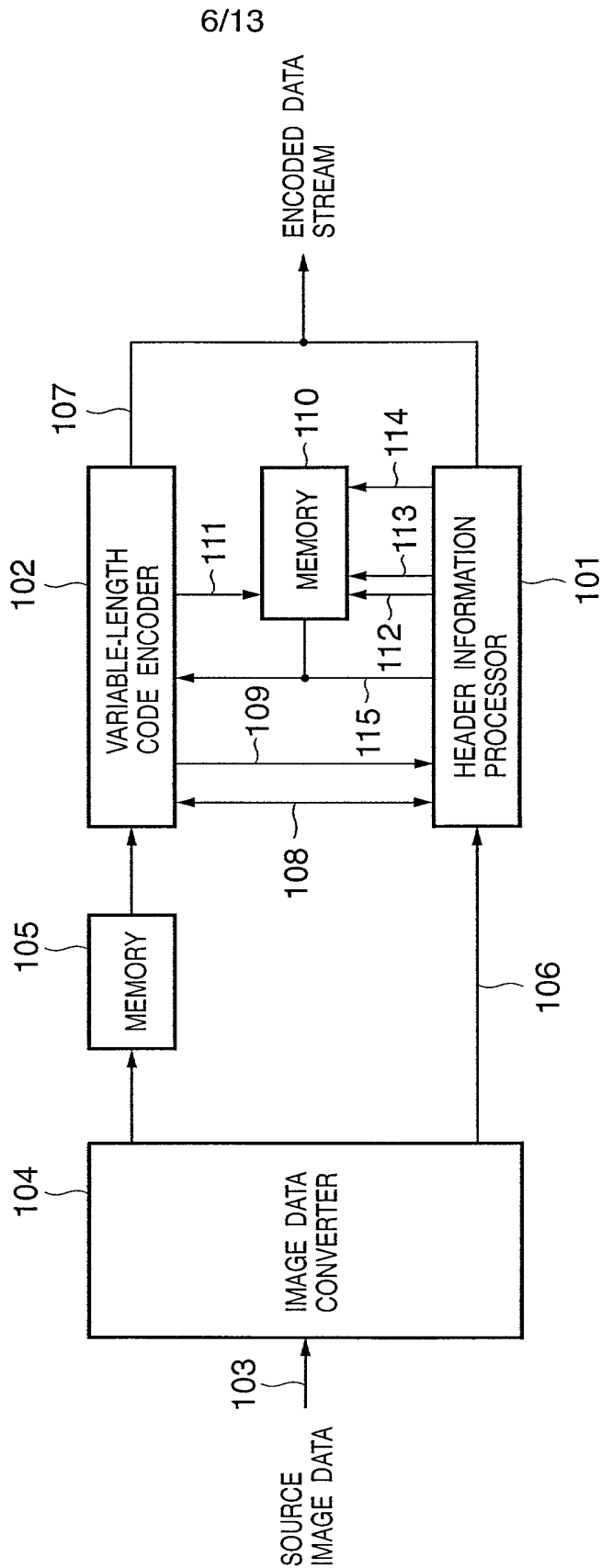


FIG. 7

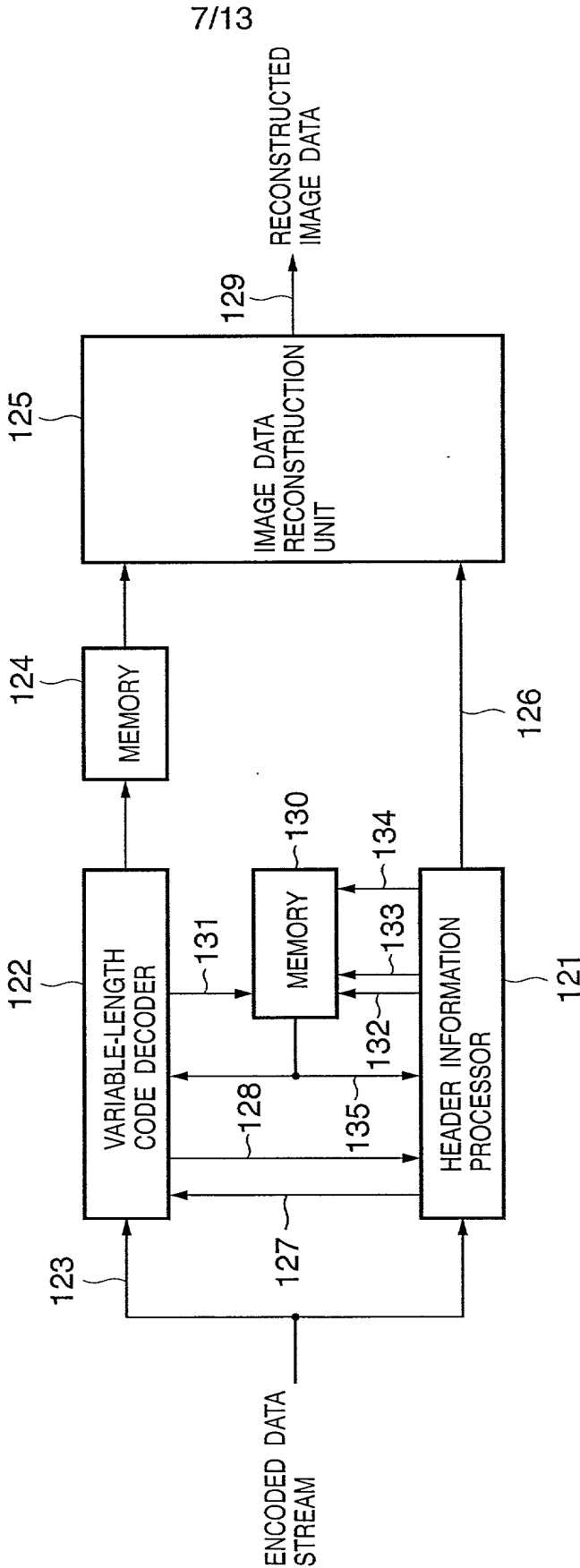
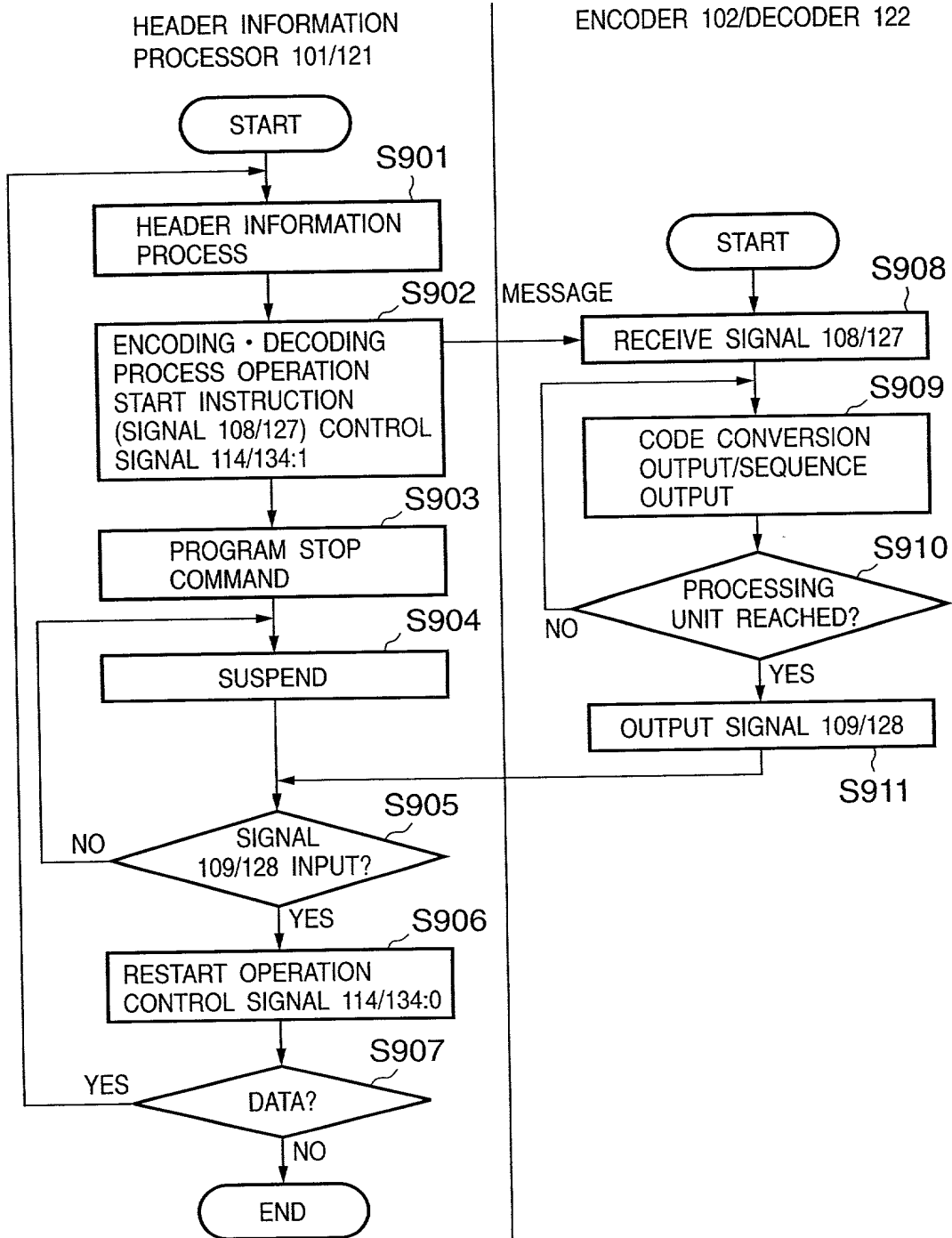


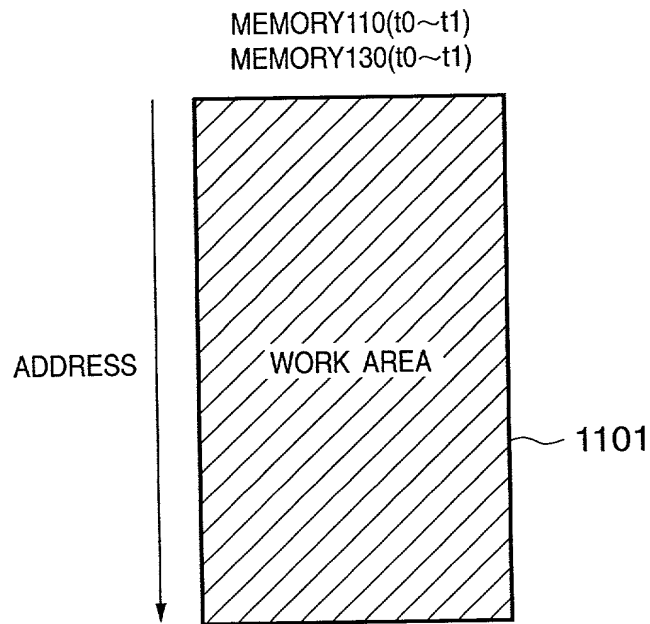




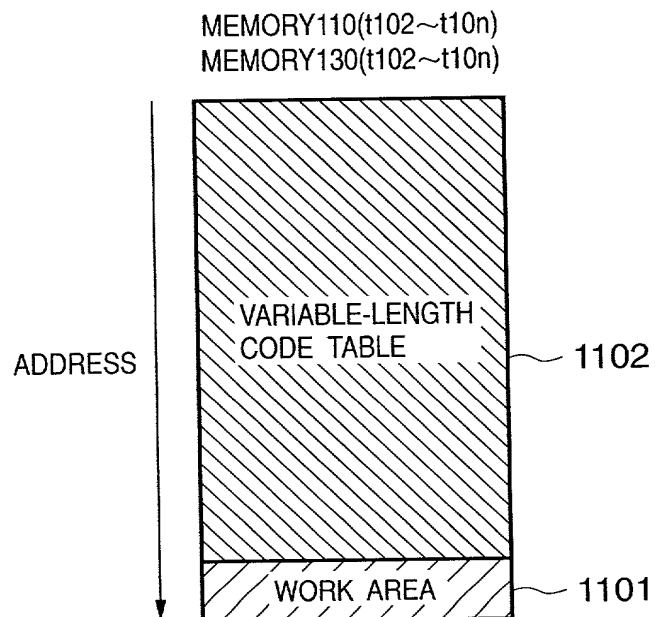
FIG. 9

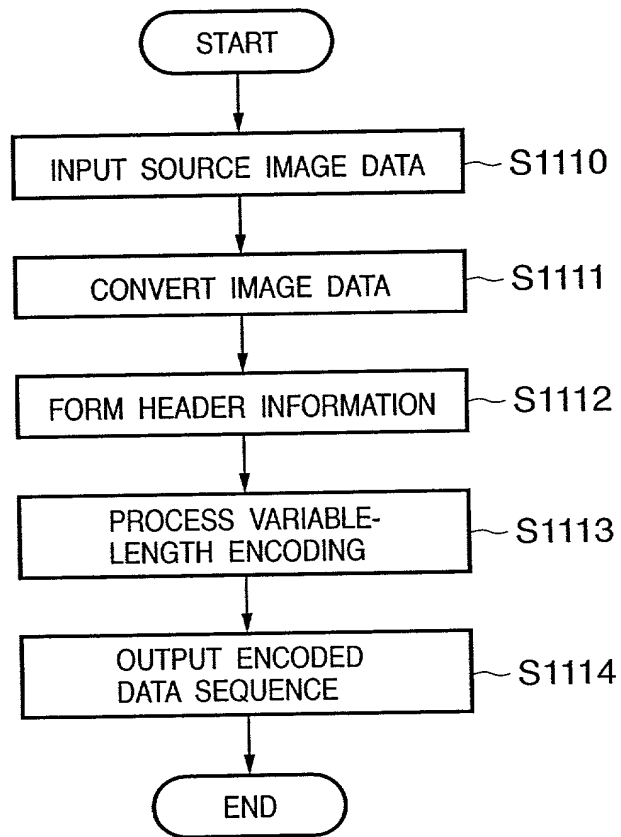


**FIG. 10A**



**FIG. 10B**



**FIG. 11A**

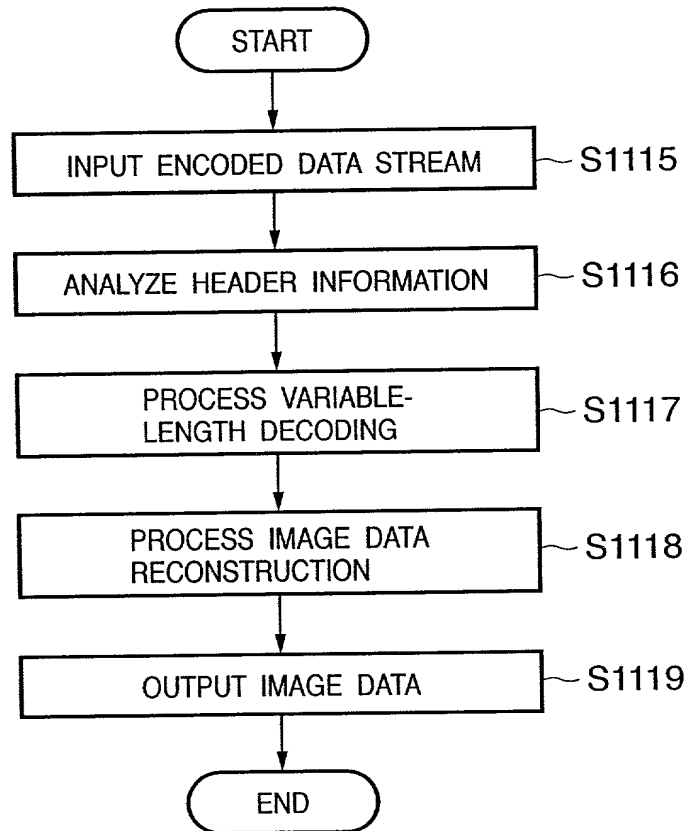
**FIG. 11B**

FIG. 12

